AMENDMENTS TO THE CLAIMS

Please cancel claim 2 without prejudice or disclaimer.

The following listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

Claims 1-3 (Cancelled)

4. (Previously Presented) A receiving device for a multipoint-to-point network, wherein the multipoint-to-point network is a synchronous multipoint-to-point CDMA network, containing a first unit for receiving and detecting a communications signal, in particular a CDMA-coded communications signal, and a second unit for receiving and detecting a coded synchronization signal, wherein the second unit contains a series circuit of a demodulator and a logical correlator and is used for demodulating and detecting the coded synchronization signal, said coded synchronization signal having a signal level which is lower than a signal level of the communications signal and/or is coded using a code which differs from a code of the communications signal, characterized in that the second unit contains two logical correlators connected in parallel for correlating the synchronization signal and two demodulators, that a first demodulator is connected in an incoming circuit to a first correlator and a second demodulator is connected in an incoming circuit to a second correlator, that the first demodulator is driven by a first clock pulse and that the second demodulator is driven by a second clock pulse, the second

clock pulse having the same clock pulse frequency as the first clock pulse and a preset phase difference compared with the first clock pulse.

5. (Previously Presented) A receiving device according to claim 4, characterized in that a delay element with a delay of half a clock pulse period is provided, said delay element is used for generating the second clock pulse from the first clock pulse.

6. (Previously Presented) A receiving device according to claim 4, characterized in that each demodulator is suitable for carrying out demodulation using alternating multiplication by +1 and -1.

7. (Previously Presented) A receiving device according to claim 4, characterized in that a selector switch for selecting one of the outputs of the correlators is connected in series to the correlators.

8. (Previously Presented) A receiving device according to claim 4, characterized in that the first clock pulse corresponds to a symbol clock pulse of a coded communications signal.

9. (Previously Presented) A receiving device according to claim 4, characterized in that the demodulator is driven by a clock pulse in which at least two phase positions can be set.